

### **REMARKS**

Claims 1 and 12 have been amended. Claim 11 has been canceled. Claims 13-16 have been added. Applicants reserve the right to pursue the original claims in this and in any other application.

Claims 1-12 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,646,894 (Hong). Applicant respectfully traverses the rejection.

Claim 1 recites a switching regulator “wherein the switching transistor, the control circuit part, and the clock signal detector circuit part are integrated into a single IC.” This is an important aspect of the invention. Please refer, for example, to page 5, lines 7-22 and page 6, lines 3-8 of Applicant’s specification. Hong does not disclose, teach or suggest this limitation. In particular, Hong does not mention how the circuitry is configured on one or more ICs and certainly does not teach that all of the circuitry will be integrated onto a single IC.


Claim 1 also recites “upon detecting stoppage of the inputting of the clock signal, the clock signal detector circuit part causes the control circuit part to stop operating.” Hong does not disclose, teach or suggest this limitation. In fact, Hong teaches that the control circuit continues to operate when in standby mode causing “the switching MOS transistor  $Q_{sw}$  [to] perform[] a burst mode operation alternately in an oscillating state and then in a quiescent state with a predetermined periodicity.” Hong, col. 7, lns 5-8. Hong, therefore, does not teach that the control circuit part stops operating in standby mode as recited in claim 1.

Hong, therefore, does not anticipate claim 1. Claims 2-10 and 12 depend from claim 1. Accordingly, the rejection should be withdrawn and the claims allowed. In addition, Applicant asserts that Hong does not teach the limitations in new claims 13-16.

In view of the above, Applicant believes the pending application is in condition for allowance.

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Respectfully submitted,

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